



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

File Copy

Applicant: Qing Ma

Title: METHOD OF FABRICATING AN INTEGRATED CIRCUIT THAT SEALS A MEMS DEVICE
WITHIN A CAVITY

Docket No.: 884.591US1

Filed: November 2, 2001

Examiner: Jamie L. Brophy

Serial No.: 10/005667

Due Date: N/A

Group Art Unit: 2822

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the attached:

☒ Communication Re: Incorrect Filing Receipt (1 pg.)

☒ Copy of Filing Receipt (1 pg.)

☒ A return postcard.

☒ Copy of Declaration and Power of Attorney (3 pgs.)

No Additional fee is required.

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P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: Ann M. McCrackin
Name: Ann M. McCrackin
Reg. No. 42,858
AMM:CMG:clh

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O.Box 1450, Alexandria, VA 22313-1450, on this 9 day of September, 2003.

Chris Hammond
Name

Chris Hammond
Signature

S/N 10/005667

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Qing Ma	Examiner:	Jamie L. Brophy
Serial No.:	10/005667	Group Art Unit:	2822
Filed:	November 2, 2001	Docket:	884.591US1
Title:	METHOD OF FABRICATING AN INTEGRATED CIRCUIT THAT SEALS A MEMS DEVICE WITHIN A CAVITY		
Assignee:	Intel Corporation	Customer No.:	21186

COMMUNICATION RE: INCORRECT FILING RECEIPT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Applicants hereby request correction of the Filing Receipt with respect to the above-identified patent application. In the Filing Receipt received August 8, 2003, (copy enclosed), the inventor information is incorrect. The Filing Receipt reads: **Oing Ma, San Jose, CA**. The Filing Receipt should read: **Qing Ma, San Jose, CA**. This is evidenced by the Declaration and Power of Attorney (copy enclosed) filed with the application.

Applicant would appreciate the above-identified printing error be corrected and that a new "corrected" filing receipt be sent to Applicant's representatives at the address given below.

Respectfully submitted,

QING MA

By his Representatives,

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Date Sept. 8, 2003 By Ann M. McCrackin
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Chris Hammond
Name

Chris Hammond
Signature



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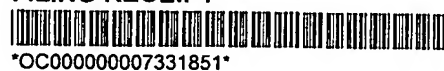
APPL NO.	FILING OR 371 (c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/005,667	11/02/2001	2822	968	884.591US1	5	28	4

CONFIRMATION NO. 7003

21186

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FILING RECEIPT



OC000000007331851



Date Mailed: 08/05/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Oing Ma, San Jose, CA;

Assignment For Published Patent Application

Intel Corporation;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 01/18/2002

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Method of fabricating an integrated circuit that seals a MEMS device within a cavity

PORTFOLIO I.P.

Preliminary Class

AUG 08 2003

RECEIVED